PRELIMINARY AMENDMENT

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a unit length signal generator which generates a periodic signal of a unit length;

a memory for temporarily storing said information data in synchronism with said periodic

signal from said unit length signal generator and supplying said information data in synchronism

with a clock signal;

a pre-pit signal reproducing circuit for detecting said pre-pits from said recording medium and generating a pre-pit signal;

a phase-locked loop circuit for generating said clock signal which is phase-locked with a jitter component contained in said pre-pit signal; and

a recording means for recording said information data supplied from said memory on said recording medium.

- 5. (New) An information data recording apparatus as claimed in claim 1, wherein said unit length corresponds to a bit interval that is specified by a recording format used for recording the information data.
- 6. (New) An information data recording apparatus as claimed in claim 1, wherein said unit period is a sync frame.
- 7. (New) An information data recording apparatus as claimed in claim 6, wherein said sync frame has a length which is 1488 times the unit length.

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8. (New) An information data recording apparatus for recording information data on an information recording medium having pre-pits which are formed at predetermined periodic intervals, said apparatus comprising:

a memory which temporarily stores said information data to be recorded on the information recording medium and supplies said information data in synchronism with a clock signal;

a pre-pit signal reproducing circuit which detects said pre-pits from said recording medium and generates a pre-pit signal;

a phase-locked loop circuit which generates said clock signal which is phase-locked with a jitter component contained in said pre-pit signal; and

a recording device which records said information data supplied from said memory on said recording medium.

9. (New) A method for recording information data on an information recording medium having pre-pits which are formed at predetermined periodic intervals, said method comprising the steps of:

medium and supplying said information data in synchronism with a clock signal;

detecting said pre-pits from said recording medium and generating a pre-pit signal;

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generating said clock signal which is phase-locked with a jitter component contained in

said pre-pit signal; and

recording said information data supplied from said memory on said recording medium.